RESISTORS • CAPS & COILS • DELAY LINES

SURFACE MOUNT PASSIVE DELAY LINES

SMP1410 SERIES - SMALL OUTLINE 14 PIN, 10-TAP

SMP1610 SERIES - SMALL OUTLINE 16 PIN, 10-TAP

FEATURES

- ❒ Economical cost, prompt delivery
- ❒ SO and Quad formats wih 50-mil pin spacing
- ❒ Available on 24mm embossed plastic Tape & Reel
- ❒ 0.002" Co-planarity
- ❒ Fast rise times
- ❒ Operating temperature: 0°C to +70°C

OPTIONS

- ❒ 28 pin surface mount J-lead quad package (.450" sq.)
- ❒ Custom circuits available
- ❒ Non-standard delay or impedance values
- ❒ Tighter tolerance or temperature coefficient
- ❒ Faster rise times
- ❒ 100-mil pin spacing available
- ❒ Military screening per MIL-D-83532

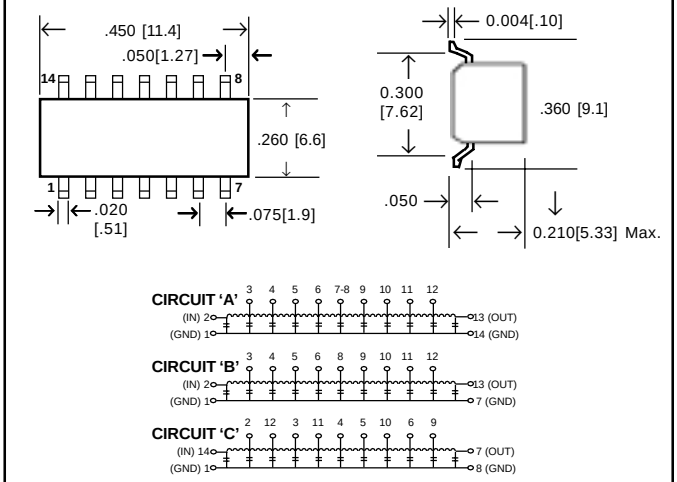
RCD SMP Series passive (analog) surface mount delay lines are a lumped constant design per applicable portions of MIL-D-23859. The series incorporates high performance inductors and multi-layer capacitors in a molded package ensuring stable transmission, low temperature coefficient, and excellent environmental performance.

Total Delay Tolerance	±5% or 1nS whichever is greater
Temperature Coefficient	100ppm/°C max.
Dielectric Strength	100VDC
Insulation Resistance	100MΩ min.
Distortion	±10% max.

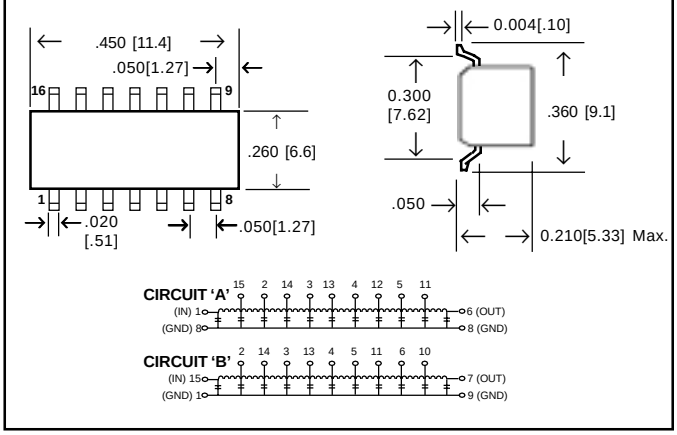
TEST CONDITIONS @ 25°C

1) Input test pulse shall have an amplitude of 3V, rise time of 3nS maximum, pulse width of 3X the total delay
2) Delay line to be terminated to within 1% of its characteristic impedance
3) Delay time measured from 50% of input pulse to 50% of output pulse
4) Rise time measured from 10% to 90% of output pulse

SMP1410



SMP1610



ELECTRICAL CHARACTERISTICS

Total Delay (nSec)	Rise Time Max. (nSec)	Delay per Tap (nSec)	Attenuation Max. (%)	Available Impedance Values (±10%)*
10	2.5	1±.5	3	50Ω, 100Ω, 200Ω
20	4	2±.5	3	50Ω, 100Ω, 200Ω
30	6	3±1	4	50Ω, 100Ω, 200Ω
40	8	4±1.5	4	50Ω, 100Ω, 200Ω
50	10	5±1.5	8	50Ω, 100Ω, 200Ω
75	15	7.5±1.5	8	50Ω, 100Ω, 200Ω
100	20	10±2	10	50Ω, 100Ω, 200Ω
125	25	12.5±2	10	50Ω, 100Ω
150	30	15±2	10	50Ω, 100Ω
200	40	20±2	10	50Ω, 100Ω

* 100Ω is the most common impedance value.

P/N DESIGNATION:

SMP1410 - 50nS - 101 A T

- **RCD Type**
- **Total Delay**
- **Impedance in 3 digit code**:(use R for decimal below 100Ω—50R=50Ω, 101=100Ω)
- **Circuit**: A, B, C
- **Packaging**: B = Bulk, T = Tape & Reel

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